

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|---|------------------|---------|------------------|
| L1 | 3314 | 365/201.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 15:55 |
| L2 | 3812 | 365/201,185.01.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 15:55 |
| L3 | 692 | 2 and (flash or eprom) and test\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 15:57 |
| L4 | 30 | 3 and ROM and CACH\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 15:58 |
| L5 | 57 | 2 and (flash adj3 memory) and micro\$1controller\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:11 |
| L6 | 42 | 5 and test\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 15:59 |
| L8 | 6 | 6 and (circuit near5 register) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:09 |
| L9 | 4176 | (flash adj3 memory) and micro\$1controller\$2 and test\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:01 |

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| L10 | 290 | 9 and (built\$1in\$1self\$1test\$3 or BIST) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:03 |
| L11 | 290 | 10 and micro\$1controller\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:04 |
| L12 | 167 | 11 and cach\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:04 |
| L13 | 118 | 12 and redundanc\$2 and counter\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:05 |
| L16 | 45 | 13 and @ad<"20030227" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:20 |
| L17 | 42 | 3 and (flash adj3 memory) and micro\$1controller\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:11 |
| L18 | 1148 | (memory adj3 device) and test\$3 and micro\$1controller\$3 and cach\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:20 |
| L19 | 1 | 18 and (data adj3 comparison adj3 circuit) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:21 |

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| L20 | 25 | 18 and (comparison adj3 circuit) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:21 |
| L21 | 15 | 20 and counter_and_driver\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:22 |
| L22 | 0 | 15 and detect\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:22 |
| L23 | 37 | kumar-promod.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:24 |
| L24 | 35 | tomaieuolo-francesco.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:25 |
| L25 | 2 | nicosia-pierpaolo.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:25 |
| L26 | 0 | ambroggi-luca.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:25 |
| L27 | 5 | pipitone-francesco.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/27 16:25 |

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| S1 | 20593 | (memory adj2 device) same (flash adj2 memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:33 |
| S2 | 754 | S1 same micro\$1controller | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:34 |
| S3 | 151 | S1 and micro\$1controller and test\$3 and redundancy | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:37 |
| S4 | 50 | S3 and (circuit or block) and cach\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:36 |
| S5 | 35 | S4 and counter and data and register | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:38 |
| S6 | 27 | S5 and driver and comparis\$3 and detec\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:38 |
| S7 | 27 | S6 and read and write and programm\$3 and interfac\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:40 |
| S8 | 12 | (("5633877") or ("5732207") or ("5058068") or ("20010004326") or ("20010016897") or ("20010019499")).PN. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/04/29 12:43 |

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| S9 | 6 | S2 and 365/201.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:44 |
| S10 | 22 | S3 and "365"/\$7.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:45 |
| S11 | 0 | kumar-francesco.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:47 |
| S12 | 0 | kumar-francesco-\$.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:47 |
| S13 | 2 | nicosia-pierpaolo.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:47 |
| S14 | 0 | nicosia-pierpaolo-\$.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:47 |
| S15 | 0 | ambroggi-luca-g.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:47 |
| S16 | 0 | ambroggi-luca.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:47 |

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| S17 | 5 | pipitone-francesco.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:48 |
| S18 | 0 | pipitone-francesco-\$:in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/04/29 12:48 |